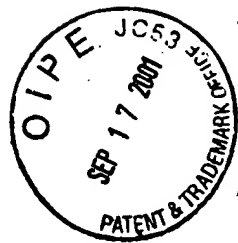


10-11-01



Inventor: John T. Moor et al.

Title: Capacitor Constructions, DRAM Constructions and  
Semiconductive Material Assemblies

Assignee: Micron Technology, Inc.

Serial No.: 09/603,147

Filed: June 23, 2000

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

**PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the references listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

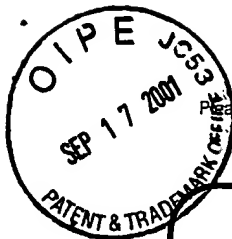
Respectfully submitted,

Date: 9/13/01

Attorney: [Signature]

David G. Latwesen, Ph.D.  
Reg. No. 38,533  
Wells, St. John, Roberts,  
Gregory & Matkin, P.S.

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(to be used for all correspondence after initial filing)

Application Number

09/603,147

Filing Date

June 23, 2000

First Named Inventor

John T. Moore et al.

Group Art Unit

2822

Examiner Name

Unknown

Total Number of Pages in This Submission

Attorney Docket Number

MI22-1443

**ENCLOSURES (check all that apply)**☐ Fee Transmittal Form☐ Fee Attached☐ Amendment / Response☐ After Final☐ Affidavits/declaration(s)☐ Extension of Time Request☐ Express Abandonment Request☒ Supplemental  
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Document(s)☐ Response to Missing Parts/  
Incomplete Application☐ Response to Missing  
Parts under 37 CFR  
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Remarks

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or  
Individual nameDavid G. Latwesen, Ph.D.; Reg. No. 38,533; Wells, St. John  
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